MOS FET Relay G3VM

Operational Principle

1. The LED lights up when the current is connected at the input side.
2. The light sent by the LED will be converted into voltage when it is received by the photodiode.
3. This voltage will be the gate voltage to drive the MOS FET via control circuit.

MOS FET Relay Feature

Ultra small size
Contributing to downsizing of customer equipment by using our compact MOS FET relay like VSON and S-VSON.

Long operating life
MOS FET Relays use light signal instead of moveable contacts, avoiding reduction of life caused by contact wear, substantially increasing operational life.

Silent operation
As MOS FET Relays do not have mechanical contacts, using a MOS FET instead of an electromechanical relay can help to eliminate switching noise in applications.

G3VM - [ ] [ ] [ ] [ ] [ ]

Load voltage | Contact form | Appearance | Additional functions | Serial code | Input forward voltage
---|---|---|---|---|---
2:20 V | 1:1a contact | A: DIP 4-pin PCB terminal | G: SOP 4-pin | L: Current limit | H: High input forward voltage
3:30 V | 2:2a contact | B: DIP 6-pin PCB terminal | H: SOP 6-pin | R: Low ON-resistance type | L: Low input forward voltage
4:40 V | 3:1b contact | C: DIP 8-pin PCB terminal | J: SOP 8-pin | Y: Dielectric strength between I/O above 2.5 kV type | V: Voltage driving type
5:50 V | 4:2b contact | D: DIP 4-pin surface-mount terminal | L: SOP 4-pin | | *Some voltage driving types only
6:60 V | 5:1a1b contact | E: DIP 6-pin surface-mount terminal | P: USOP 4-pin | | |
7:75 V | 6:600 V | F: DIP 8-pin surface-mount terminal | Q: S-VSON 4-pin | | |
8:80 V | | | R: S-VSON 4-pin | | |
9:100 V | | | S: SSOP 4-pin | | |
10:200 V | | | T: S-SON 4-pin | | |
20:200 V | | | U: VSON 4-pin | | |
35:350 V | | | V: VSON 4-pin | | |
40:400 V | | | W: P-SON 4-pin | |
### G3VM Main Package Types

<table>
<thead>
<tr>
<th>Package Type</th>
<th>Mounting Area</th>
</tr>
</thead>
<tbody>
<tr>
<td>DIP</td>
<td>100%</td>
</tr>
<tr>
<td>SOP</td>
<td>62%</td>
</tr>
<tr>
<td>SSOP</td>
<td>24%</td>
</tr>
<tr>
<td>USOP</td>
<td>20%</td>
</tr>
<tr>
<td>P-SON</td>
<td>19%</td>
</tr>
<tr>
<td>VSON(R)</td>
<td>10%</td>
</tr>
<tr>
<td>VSON</td>
<td>9%</td>
</tr>
<tr>
<td>S-VSON</td>
<td>8%</td>
</tr>
</tbody>
</table>

*84% of VSON

**DIP (Dual Inline Package)**

- **DIP8**: 9.66±0.25
- **DIP6**: 7.12±0.25
- **DIP4**: 4.58±0.25

**SOP (Small Outline Package)**

- **SOP8**: 9.4±0.25
- **SOP6**: 6.3±0.25
- **SOP4**: 3.9±0.25
- **Special SOP4**: 4.5±0.25

Please check each region's Terms & Conditions by region website.

The unspecified dimension tolerance is ±0.1 mm.

K345-E1-01(0722)