

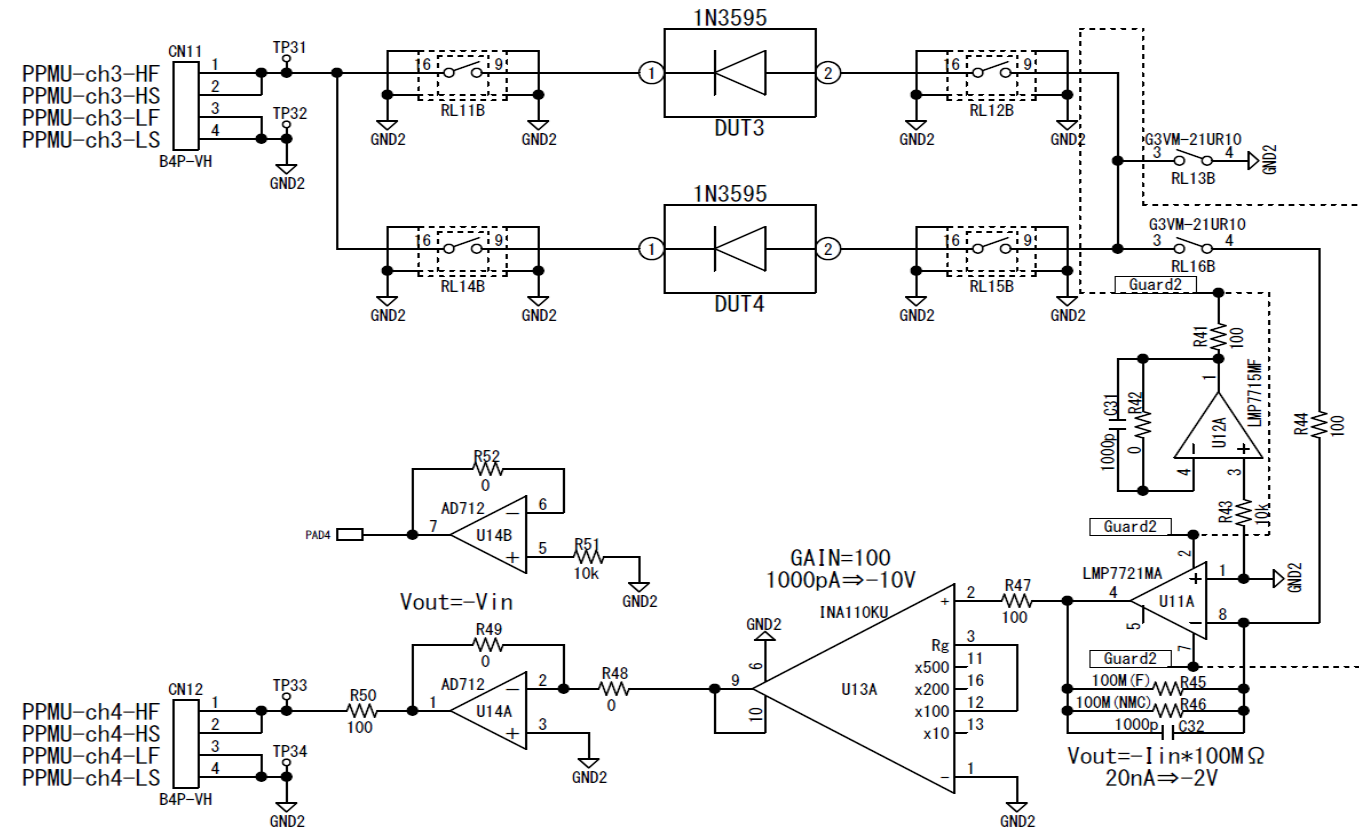
[illegible]

The schematic diagram illustrates a 6-bit digital-to-analog converter (DAC) circuit. It features two comparators, U5A and U5B (SN74LVC2G06), and two 3-bit DACs, U5C and U6C (SN74LVC2G06). The circuit is powered by a +5V supply. The input is a 6-bit digital signal (CBIT1-CBIT6) connected to a switch (SW1) and a 6-bit digital output (CBIT1-CBIT6). The output is connected to a 6-bit digital input (CBIT1-CBIT6) and a 6-bit digital output (CBIT1-CBIT6). The circuit includes resistors (R17, R18, R19, R20, R21, R22, R23, R24, R25, R26, R27, R28, R29, R30), capacitors (C11, C12), and a switch (SW1). The output is connected to a 6-bit digital input (CBIT1-CBIT6) and a 6-bit digital output (CBIT1-CBIT6).

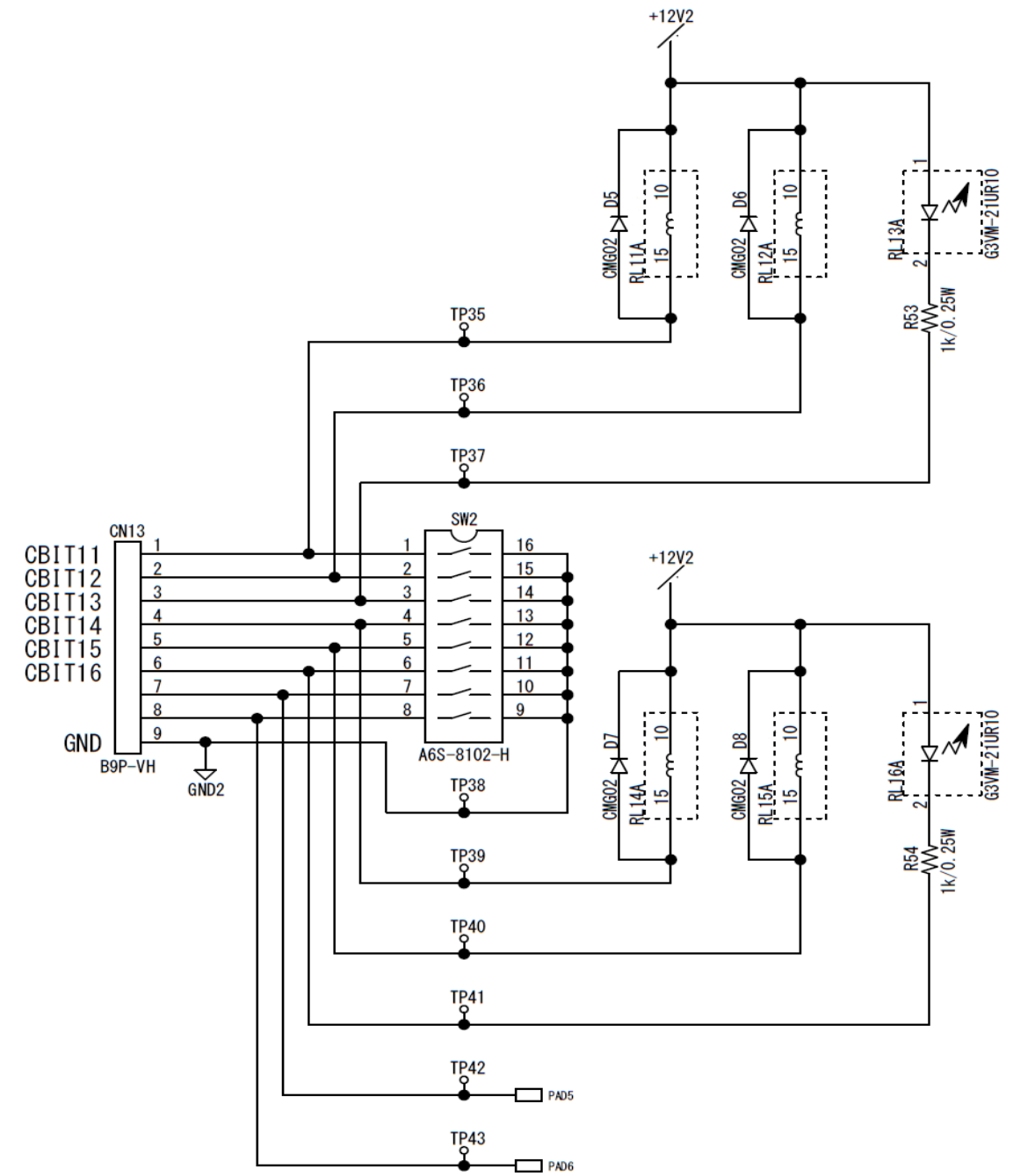
A3

② Using REED-RELAY

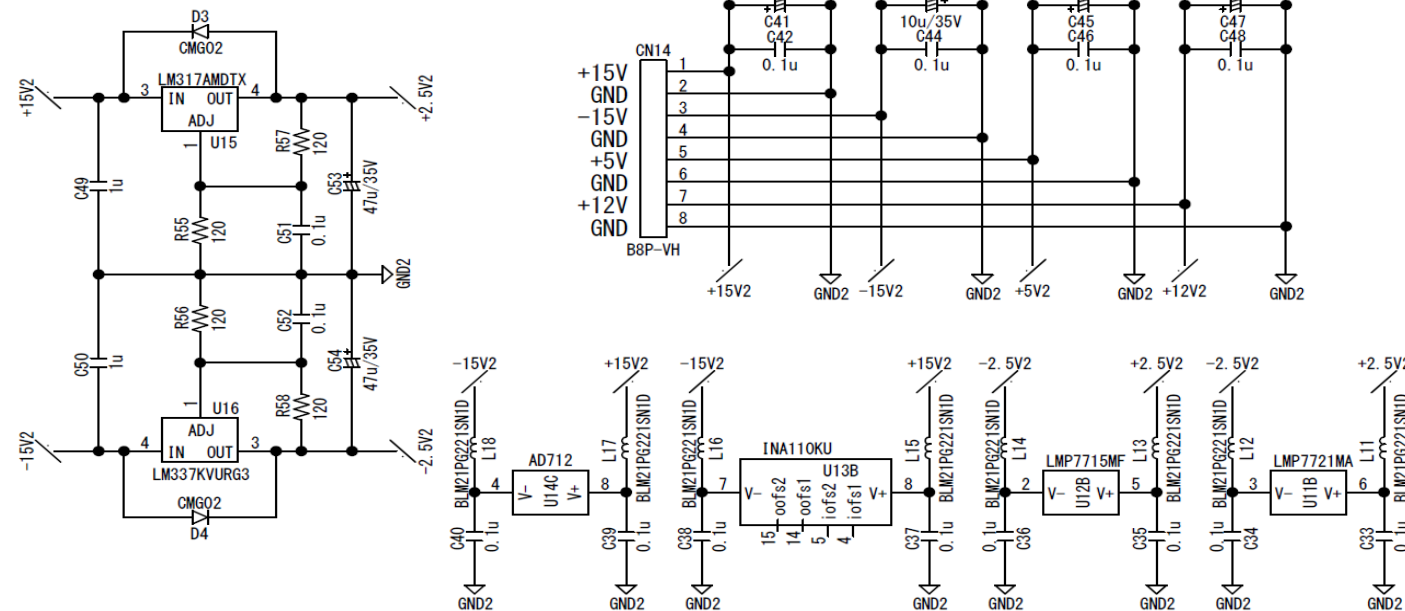
DC test unit



Relay Control



Others

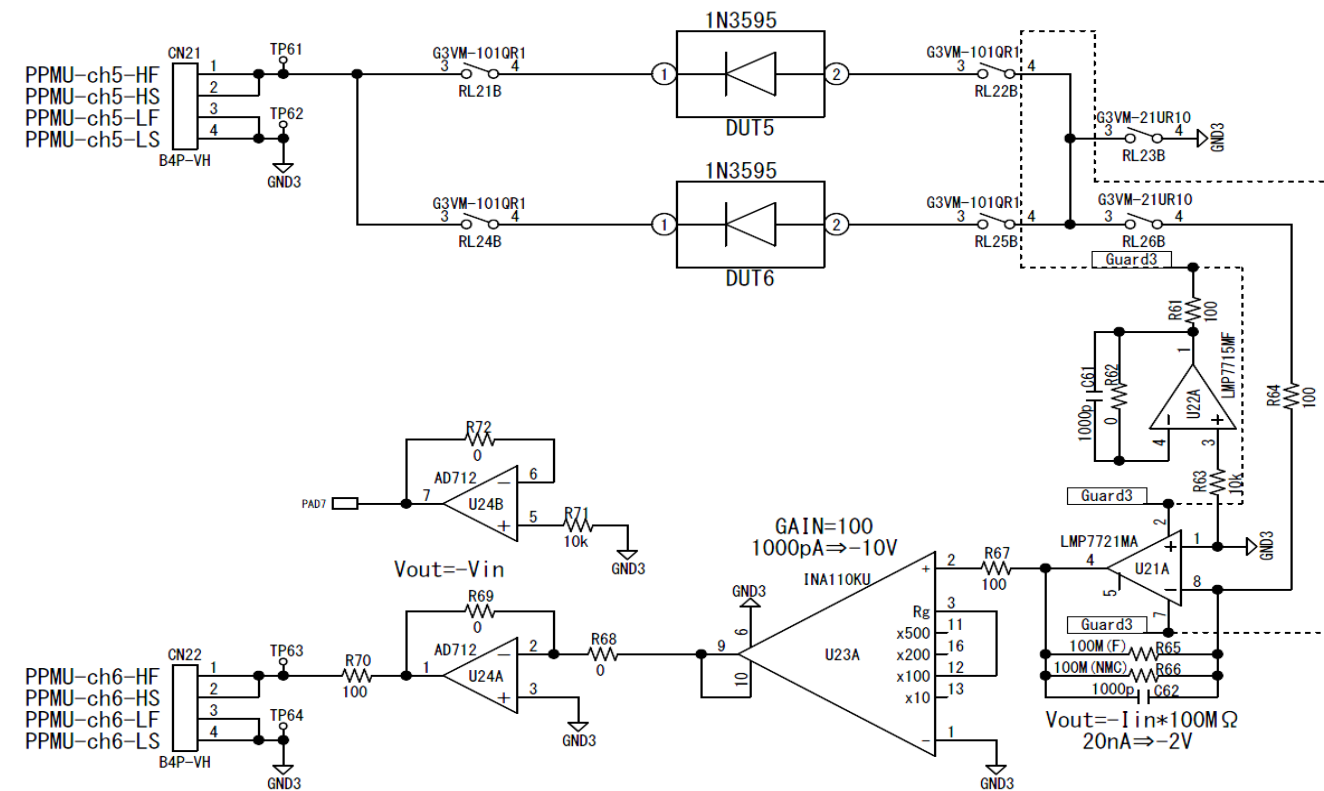


G3VM-101MT REFERENCE DESIGN CIRCUIT DIAGRAM

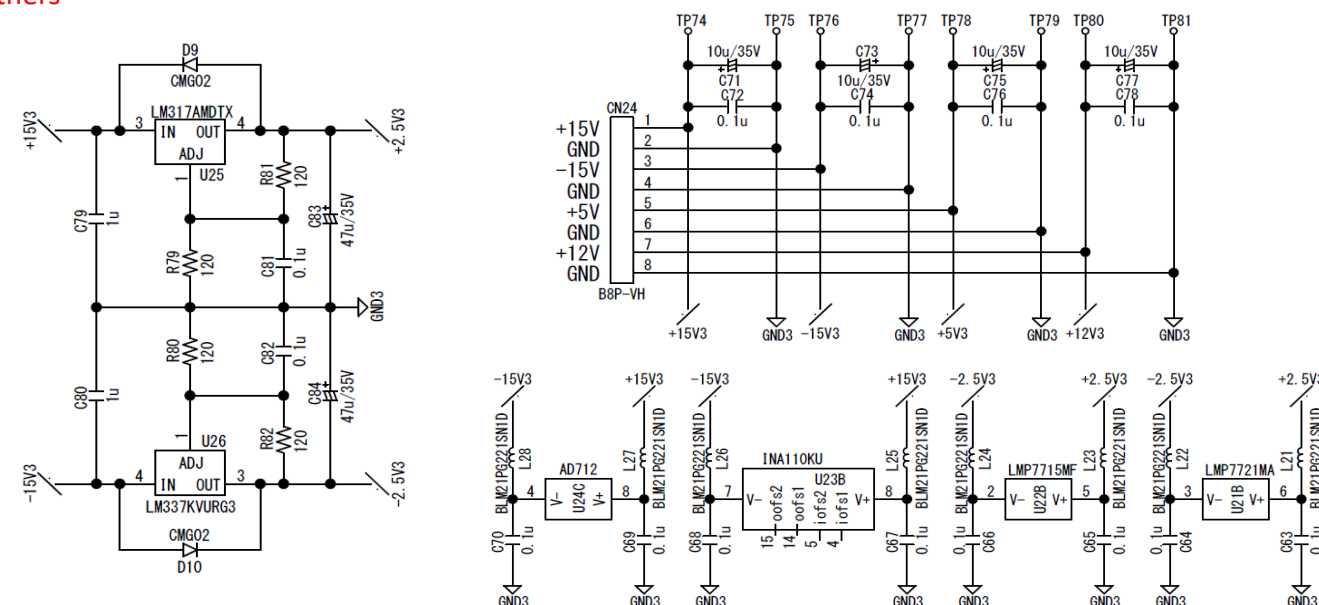
SHEET	DRWG NO.	Rev.
2 / 3	YPE-G3VM-0624	A
DESIGNED FOR		DC Parametric measurement

③ Using MOSFET-RELAY

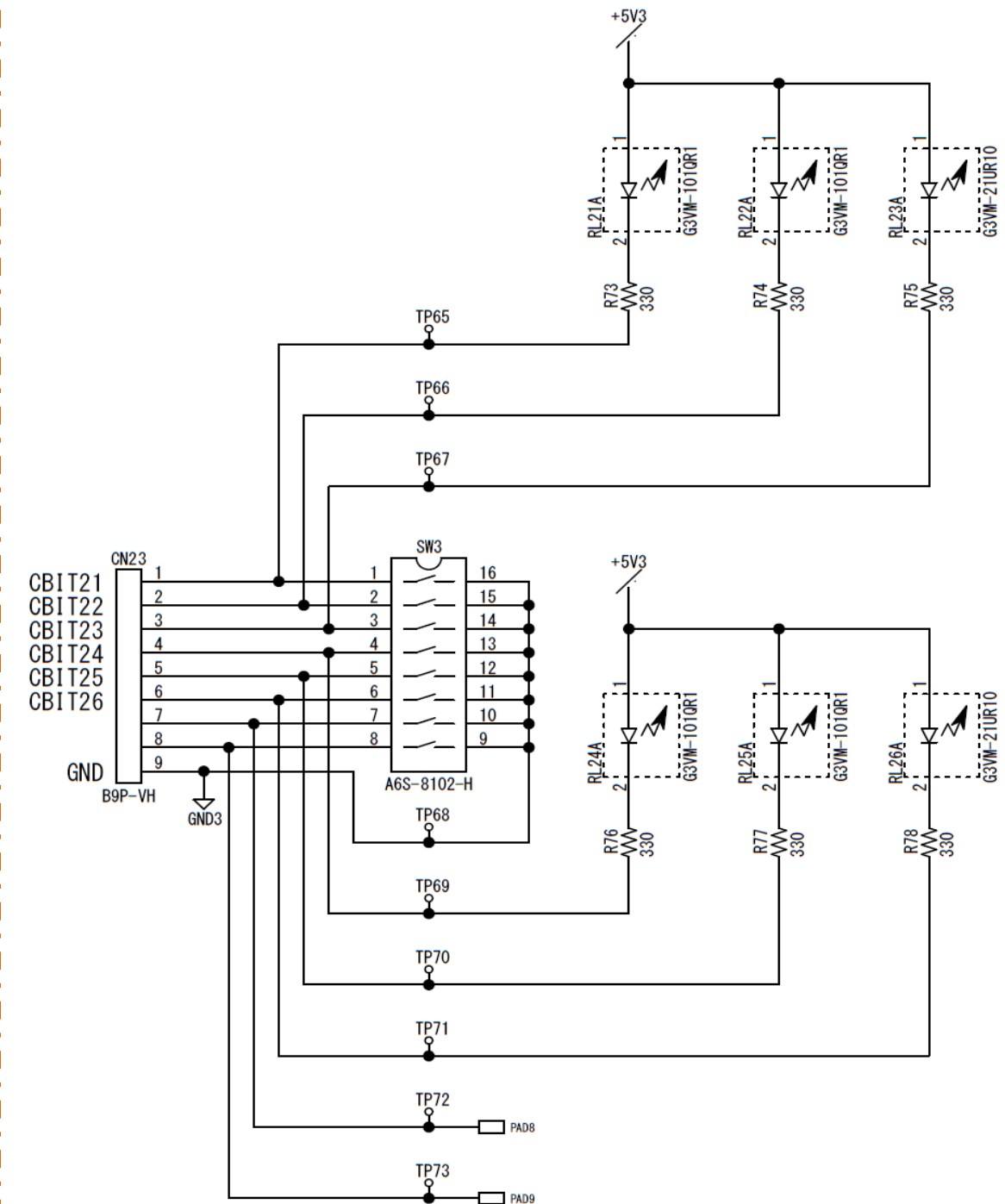
DC test unit



Others



Relay Control



G3VM-101MT REFERENCE DESIGN CIRCUIT DIAGRAM

SHEET	DRWG NO.	Rev.
3 / 3	YPE-G3VM-0624	A
DESIGNED FOR		DC Parametric measurement